PTO/SB/08A (07-06)

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Complete if Known Substitute for form 1449A/PTO Application Number 10/631,071 07/31/2003 Filing Date INFORMATION DISCLOSURE Ronald L. Mahany First Named Inventor STATEMENT BY APPLICANT Group Art Unit 2617 **Examiner Name** M. Santiago Cordero (use as many sheets as necessary) Attorney Docket Number 14406US02 2 of Sheet

Examiner Initial*	Cite No.1	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner Initials*	Cite No. 1	Foreign Patent Document Country Code³-Number ⁴-Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T⁵
	B4	WO 92/02084	02/06/1992	Koenck		

		OTHER ART NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	
	C433	"Radio Networks-An Air Bridge For Data", 1989 no date available	
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Complete if Known Substitute for form 1449A/PTO 10/631,071 Application Number INFORMATION DISCLOSURE Filing Date 07/31/2003 Ronald L. Mahany First Named Inventor STATEMENT BY APPLICANT Group Art Unit 2617 M. Santiago **Examiner Name** (use as many sheets as necessary)) Cordero 14406US02 Attorney Docket Number 2 Of Sheet

		OTHER ART NON PATENT LITERATURE DOCUMENTS
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	C497	Radio Data Link Access Procedure published by Motorola, 03/1991
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	C578	KISHINE et al., "A 2.5 Gb/s Clock And Data Recovery IC With Tunable Jitter Characteristics For Use In LAN's And WAN's", IEEE J.of Solid-State Circuits, 06/1999, Vol. 34, No. 6, pp. 805-812
	C579	KRAL et al., "RF-CMOS Oscillators With Switched Tuning", Custom IC Conference, Santa Clara, CA, 05/1998, pp. 555-558
	C580	KRAL, "A 2.4 GHz CMOS Frequency Synthesizer", UCLA Integrated Circuits And Systems Laboratory, 03/1998
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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /MSC/

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